

SRM20256L_{10/12}

CMOS 256K-BIT STATIC RAM

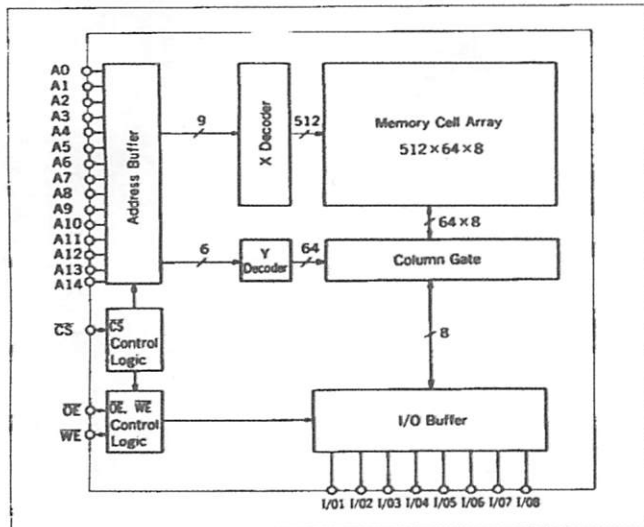
DESCRIPTION

The SRM20256L_{10/12} is a 32,768 words x 8 bits asynchronous, static, random access memory fabricated using an advanced CMOS technology. Its very low standby power requirement makes it ideal for applications requiring non-volatile storage with back-up batteries. The asynchronous and static nature of the memory requires no external clock or refresh circuit. Input and output ports are TTL compatible and the three-state output allows easy expansion of memory capacity.

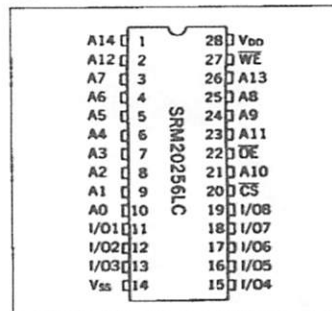
FEATURES

- Fast access time SRM20256L₁₀ 100ns (Max)
SRM20256L₁₂ 120ns (Max)
- Low supply current Standby : 2 μ A (Typ)
Operation: 13mA/1MHz (Typ)
- Completely static No clock required
- Single power supply 5V \pm 10%
- TTL compatible inputs and outputs
- 3-state output
- Battery back-up operation
- Package SRM20256LC_{10/12} 28-pin DIP (plastic)
SRM20256LM_{10/12} 28-pin SOP (plastic)
SRM20256LS_{10/12} 28-pin Shrink DIP (plastic)
SRM20256L_{10/12} 28-pin TSOP (plastic)

BLOCK DIAGRAM



PIN CONFIGURATION



PIN DESCRIPTION

A0 to A14	Address Input
WE	Write Enable
OE	Output Enable
CS	Chip Select
I/O1 to 8	Data Input/Output
VDD	Power Supply (+5V)
VSS	Power Supply (0V)